The Gilgamesh Processor Board – A PIM Prototyping Tool

By

GARY BLOCK

Thursday, March 27, 2003

12:00 noon – 1:00 p.m.

Building 126, Room 225

The Gilgamesh Processor Board is intended to provide researchers with a rapid prototyping platform for assessing Processor-in-Memory (PIM) architectures. The board allows designers to evaluate architectural and hardware design choices, software structures, and performance on a hardware-based platform.

The Gilgamesh Processor Board is designed to offer a flexible development platform to provide PIM architecture development with the freedom to explore design changes and evolve the design as the project matures. At the end of the experimental phase, the researcher will have a complete Verilog model that can be used as the basis for the development of an ASIC part.

For questions, please contact Hans Zima at 4-8980.